

coupled to additional I/O port(s) **1013**, which are coupled to the system bus **1002** through the I/O controller **1010**. Additional ports or devices, such as serial ports, parallel ports, firewire adapters, or biometric devices (not shown), may be utilized through the I/O controller **1010**. A display device (not shown) can be coupled to a display device port **1014** which is coupled to the system bus **1002** through the video controller **1015**. A network device (not shown), including but not limited to an Ethernet device or other device having networking capability, can be coupled to a network port **1020** which is coupled through the network controller **1016** to the system bus **1002**. The computer system **1000** may be wirelessly coupled to a network device that is configured for wireless operation (not shown), including but not limited to wireless routers, using an antenna **1028** coupled to a wireless controller **1026** coupled to the system bus **1002**, where the antenna transmits/receives signals to/from the network device. The computer system **1000** may include one or more USB ports **1023**. A USB device (not shown), including but not limited to a printer, scanner, keyboard, mouse, digital camera, storage device, PDA, cellular phone, biometric device, webcam, and I/O adapters can be coupled to the USB port **1023** which is coupled to the system bus **1002** through the USB controller **1011**. Other devices, such as cellular phones, PDAs, and other portable devices may also be coupled wirelessly via a wireless I/O antenna **1032** that is coupled to a wireless I/O controller **1030**. Examples of wireless I/O technologies include, but are not limited to, Bluetooth, Infrared (IR), and Radio-Frequency (RF). Audio devices, such as microphones, speakers, or headphones may be coupled to a sound port **1038** that is coupled to a sound controller **1034** that is coupled to the system bus **1002**. Expansion slots **1018** can include Industry Standard Architecture (ISA) slots, Peripheral Component Interconnect (PCI) expansion slots, PCI Express expansion slots, Accelerated Graphics Port (AGP) slots or any other slot generally known in the art to allow additional cards to be placed into the computer system **1000**. These slots can be used to couple network cards, video cards, sound cards, modems and any other peripheral devices generally used with a computer. The computer system **1000** also includes a source of power (not shown), including but not limited to a power supply coupled to an external source of power, and/or an internal or external battery. Detailed descriptions of these devices have been omitted for convenience only and should not be construed as limiting.

[0036] The embodiments of the present disclosure may be implemented with any combination of hardware and software. If implemented as a computer-implemented apparatus, the embodiment is implemented using means for performing all of the steps and functions described above.

[0037] The embodiments of the present disclosure can be included in an article of manufacture (e.g., one or more computer program products) having, for instance, computer useable media. The media has embodied therein, for instance, computer readable program code means for providing and facilitating the mechanisms of the embodiments of the present disclosure. The article of manufacture can be included as part of a computer system or sold separately.

[0038] There is little distinction left between hardware and software implementations of aspects of systems; the use of hardware or software is generally (but not always, in that in certain contexts the choice between hardware and software can become significant) a design choice representing cost vs. efficiency tradeoffs. There are various vehicles by which pro-

cesses and/or systems and/or other technologies described herein can be effected (e.g., hardware, software, and/or firmware), and that the preferred vehicle will vary with the context in which the processes and/or systems and/or other technologies are deployed. For example, if an implementer determines that speed and accuracy are paramount, the implementer may opt for a mainly hardware and/or firmware vehicle; if flexibility is paramount, the implementer may opt for a mainly software implementation; or, yet again alternatively, the implementer may opt for some combination of hardware, software, and/or firmware.

[0039] The foregoing detailed description has set forth various embodiments of the devices and/or processes via the use of block diagrams, flowcharts, and/or examples. Insofar as such block diagrams, flowcharts, and/or examples contain one or more functions and/or operations, it will be understood by those within the art that each function and/or operation within such block diagrams, flowcharts, or examples can be implemented, individually and/or collectively, by a wide range of hardware, software, firmware, or virtually any combination thereof. In some embodiments, several portions of the subject matter described herein may be implemented via Application Specific Integrated Circuits (ASICs), Field Programmable Gate Arrays (FPGAs), digital signal processors (DSPs), or other integrated formats. However, those skilled in the art will recognize that some aspects of the embodiments disclosed herein, in whole or in part, can be equivalently implemented in integrated circuits, as one or more computer programs running on one or more computers (e.g., as one or more programs running on one or more computer systems), as one or more programs running on one or more processors (e.g., as one or more programs running on one or more microprocessors), as firmware, or as virtually any combination thereof, and that designing the circuitry and/or writing the code for the software and/or firmware would be well within the skill of one of skill in the art in light of this disclosure. In addition, those skilled in the art will appreciate that the mechanisms of the subject matter described herein are capable of being distributed as a program product in a variety of forms, and that an illustrative embodiment of the subject matter described herein applies regardless of the particular type of signal bearing medium used to actually carry out the distribution. Examples of a signal bearing medium include, but are not limited to, the following: a recordable type medium such as a flexible disk, a hard disk drive (HDD), a Compact Disc (CD), a Digital Video Disk (DVD), a digital tape, a computer memory, etc.; and a transmission type medium such as a digital and/or an analog communication medium (e.g., a fiber optic cable, a waveguide, a wired communications link, a wireless communication link, etc.).

[0040] Those skilled in the art will recognize that it is common within the art to describe devices and/or processes in the fashion set forth herein, and thereafter use engineering practices to integrate such described devices and/or processes into data processing systems. That is, at least a portion of the devices and/or processes described herein can be integrated into a data processing system via a reasonable amount of experimentation. Those having skill in the art will recognize that a typical data processing system generally includes one or more of a system unit housing, a video display device, a memory such as volatile and non-volatile memory, processors such as microprocessors and digital signal processors, computational entities such as operating systems, drivers, graphical user interfaces, and applications programs, one or